

SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

ATP206

N-Channel Silicon MOSFET General-Purpose Switching Device Applications

Features

- Low ON-resistance
- 4.5V drive
- Halogen free compliance

Specifications

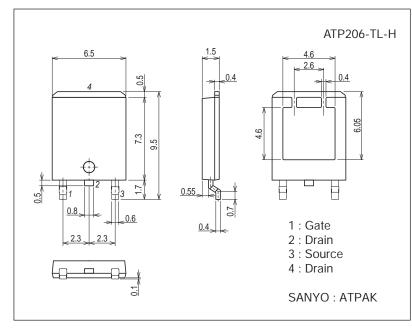
Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		40	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		40	А
Drain Current (PW≤10μs)	IDP	PW≤10μs, duty cycle≤1%	120	А
Allowable Power Dissipation	PD	Tc=25°C	40	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C
Avalanche Energy (Single Pulse) *1	EAS		26	mJ
Avalanche Current *2	IAV		20	А

Note :*1 V_{DD}=10V, L=100µH, I_{AV}=20A *2 L≤100µH, Single pulse

Package Dimensions

unit : mm (typ) 7057-001



Product & Package Information

- Package : ATPAK
- JEITA, JEDEC
- Minimum Packing Quantity : 3,000 pcs./reel

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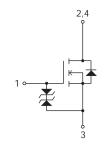
Packing Type: TL





Marking

Electrical Connection



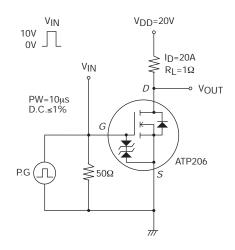
SANYO Semiconductor Co., Ltd. http://semicon.sanyo.com/en/network

- Large current
- Slim package
- · Protection diode in

Electrical Characteristics at Ta=25°C

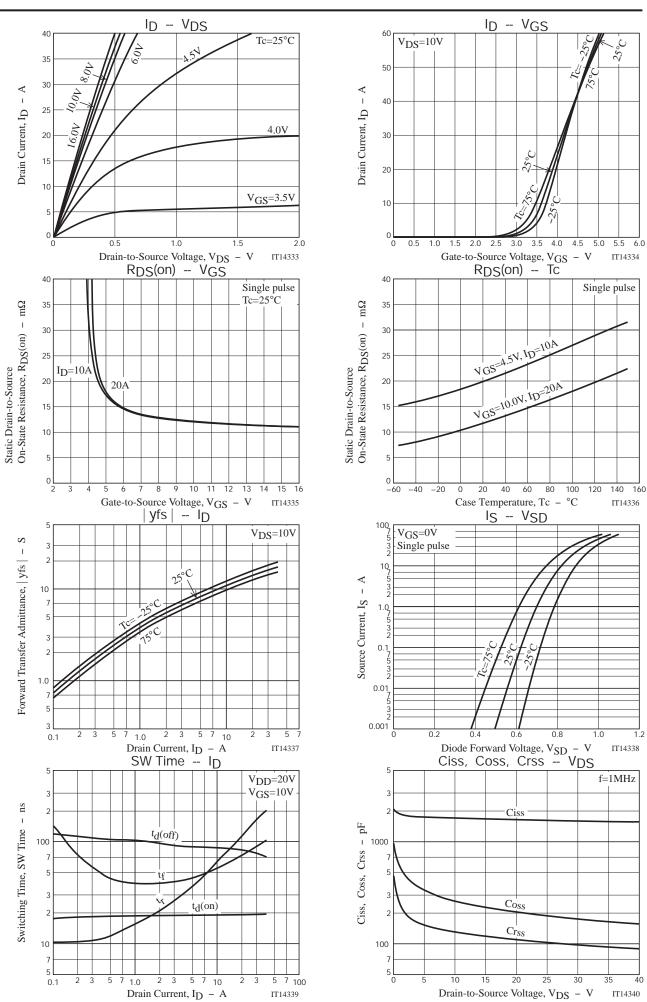
Daramatar	Symbol	Conditions	Ratings			Unit	
Parameter	Symbol	Conditions	min	typ	max		
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0V	40			V	
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =40V, V _{GS} =0V			1	μΑ	
Gate-to-Source Leakage Current	IGSS	V _{GS} =±16V, V _{DS} =0V			±10	μΑ	
Cutoff Voltage	V _{GS} (off)	V _{DS} =10V, I _D =1mA	1.5		2.6	V	
Forward Transfer Admittance	yfs	VDS=10V, ID=20A	9	15		S	
Static Drain-to-Source On-State Resistance	R _{DS} (on)1	ID=20A, VGS=10V		12	16	mΩ	
Static Drain-to-Source On-State Resistance	R _{DS} (on)2	ID=10A, VGS=4.5V		20	28	mΩ	
Input Capacitance	Ciss			1630		рF	
Output Capacitance	Coss	V _{DS} =20V, f=1MHz		205		рF	
Reverse Transfer Capacitance	Crss			110		рF	
Turn-ON Delay Time	t _d (on)			19		ns	
Rise Time	tr	See encolfied Test Circuit		110		ns	
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		83		ns	
Fall Time	tf			73		ns	
Total Gate Charge	Qg			27		nC	
Gate-to-Source Charge	Qgs	V _{DS} =20V, V _{GS} =10V, I _D =40A		7.0		nC	
Gate-to-Drain "Miller" Charge	Qgd			5.2		nC	
Diode Forward Voltage	VSD	IS=40A, VGS=0V		0.99	1.2	V	

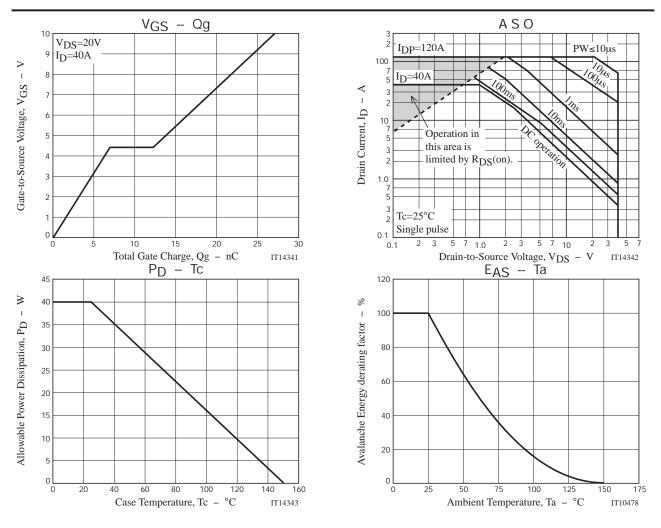
Switching Time Test Circuit



Ordering Information

Device	Package	Shipping	memo
ATP206-TL-H	ATPAK	3,000pcs./reel	Pb Free and Halogen Free

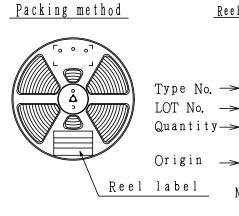




Taping Specification ATP206-TL-H

1. Packing Format (TL)

Package Name	Carrier Tape	Maximum Number of devices contained (po		Carrier Tape Maximum Number of devices contained (pcs)		Packing f	Packing format	
Larvade Mame	Туре	Reel	Inner box	Outer box	INNER BOX SD-C-18	OUTER BOX SD-A-18		
					1 reels contained	5 inner boxes contained		
ATPAK	ATP	3,000	3,000	15,000	Dimensions:mm (external)	Dimensions:mm (external)		
					340×340×28	355×355×165		



Reel	label, Inner box label	<u>e l</u>
	(unit:mm)	

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Outer box label It is a label at the time of factory shipments. The form of a label may change in physical distribution process.

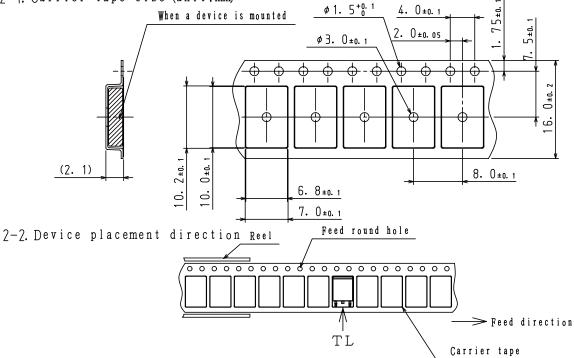
	L	-	108
	[•
		TYPE CODE	***************************************
		TYPE	00000000
		QTY	0,000 pcs (1) Lead free #
$\overline{\infty}$		LOT	00000000
		PACKAGE	00000000
-	,	SPECIAL ASSEMBLY:	*20722005310C* **** (DIFFUSION:*****)

NOTE (1) The LEAD FREE * description shows that the surface treatment of the terminal is lead free.

Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

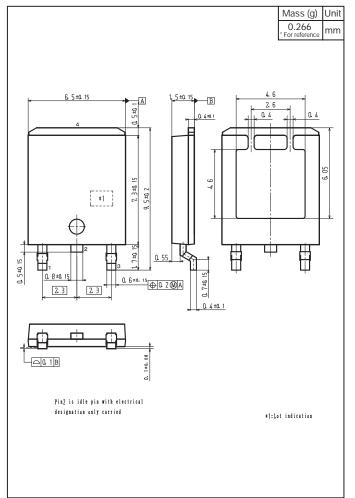
2. Taping configuration

2-1. Carrier tape size (unit:mm)

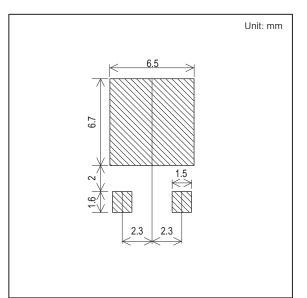


The one erectrode terminals on feed hole side ···· TL

Outline Drawing ATP206-TL-H



Land Pattern Example



Note on usage : Since the ATP206 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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